

## Wireless communication design verification engineer

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Company: MediaTek

Location: Zhubei City

Category: other-general

Job Description 無線通訊設計驗證

包含 Test plan 規劃, Bench, VIP 建立, test case 撰寫, 完成coverage等相關驗證工作Requirement1.

具有數位IC的前段驗證經驗或興趣

2. 熟悉驗證流程, 包含testbench規劃與撰寫, test plan的制定, 驗證執行, 和提高完成驗證coverage的經驗

3. 必須至少熟悉一種先進驗證方法, 如UVM, VMM, OVM, 並具有實作constraint random based verification的經驗, 包含 OOP, functional coverage, assertion checker/coverage, virtual interface.

4. 必須至少精通一種硬體驗證語言, 如System Verilog, system C, Vera

5. 具有通訊MAC layer相關知識或背景者尤佳, 如 Wifi, Ethernet, Switch 等

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